IN THE SPECIFICATION:

Please amend the following paragraphs as indicated:

[0020] When, further, a database is provided storing instructions <u>on</u> how to handle specific breakdown cases, i.e., collecting a plurality of different error state conditions of the plurality of subsystems and processors mapped thereto, then a predetermined, free configurable emergency task distribution is established saying which processor supplies which application. Thus, the application specific chips ASICs can be prioritized, and further, a priority rule can be made dependent and dynamically varying in dependence of the current operational state of the car. Thus, operation states e.g., driving with a speed of 150 km/h may be set in contrast to an operational state, of speed=0 km/h, or a closed, parking car, without any person sitting inside.

[0021] The extracted application-specific logic means may <u>be</u> advantageously implemented as in ASIC form.

[0022] Further, ~~advantageously,~~ a further, separate emergency controller, e.g., a separate ASIC chip or <u>ASIC chip</u> added to the single or multiple I/O ASIC chip solution, may be provided continuously storing the current GPS coordinates after reading it from an interface to a built-in navigation system. Said emergency controller is dedicated to send an emergency signal including said coordinates and possibly further individually programmable text information—for example specifying the blood type (A, B, AB including Rhesus factor) of the driver and accompanying persons, in form of an SMS—message (Short message Service), in case one or more external

sensor devices detect an emergency case in form of an accident. In this variation the power supply for the emergency controller preferably includes a condenser device instead of or in conjunction with a battery, in order to guarantee that the message may be sent out even in a case in which the car's main power supply does not work anymore.

[0035] In FIG. 4 a preferred embodiment of the present invention is described with varying aspects from figure to figure, which might be used in the telematics field in order to improve the plurality of embedded systems in a car. The general aspect thereof is basically coincident with the approach given in FIG. 3. It should be noted, however, that in FIGS. 4 to [[8]] 5 the standard processor units are depicted at a respective top-portion in the figures, whereas in FIGS. 1 to 3 the processors are depicted in the respective bottom portion.

[0044] All of the above-mentioned parameter registers 82, 84, 85, 86, 87, and 88 are accessible by said general controller unit 12. Preferably, said controller unit 12 comprises an own processor 80 acting independently of the plurality of processors 40 mentioned above. Further, according to a preferred aspect of the present invention the general purpose controller 12 may access a built-in "database", which stores instructions[[,]] on how to handle specific breakdown cases [[of]] or error state cases associated with either of said standard processors. For example, when some of the watchdogs monitoring the processors report an error status of the associated processor, the general controller unit will perform a lookup in the error case database and selects select the pre-programmed instruction[[,]] on how to handle this specific error case. In practice, when for example the processor associated with closing and opening the windows

has a breakdown, then the general controller unit 12 may decide due to said lookup that the processor associated basically with managing audio/video subsystems may be exceptionally used for closing the windows. This dynamically performed switching from one processor to the other processor is performed via the autonomic control switchboard matrix 62, which is basically a multiplexer controlled by unit 12. It should be understood that functions other than the above mentioned examples may also be controlled by the inventive feature of dynamic switching.

[0045] Further, preferably one or more internal controllers 110 , 112 may be advantageously provided in order to provide processing power for major emergency cases in which a significant number of standard processors are in error state. These additional controllers are then occupied to reboot the system if required, with strongly ~~modifies~~ modified emergency parameter sets, thus to perform a "reverse" boot or emergency boot process targeted to supply processing power only to the most important subsystems. This is again switched via said multiplexer unit 62 in co-operation with the general controller unit 12.

[0048] Computer program means or computer program in the present context ~~mean~~ means any expression, in any language, code or notation, of a set of instructions intended to cause a system having an information processing capability to perform a particular function either directly or after either or both of the following: